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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.		
10/051,359 01/18/2002		Mitsuru Asano	09792909-5303	9291		
26263 . 75	590 12/15/2006		EXAM	EXAMINER		
SONNENSCI	HEIN NATH & ROS	KUMAR, SR	KUMAR, SRILAKSHMI K			
P.O. BOX 0610				D. DDD 14704DD		
WACKER DRIVE STATION, SEARS TOWER			ART UNIT	PAPER NUMBER		
CHICAGO, IL	60606-1080		2629			

DATE MAILED: 12/15/2006

Please find below and/or attached an Office communication concerning this application or proceeding.

			 					
		Application	Application No. Applicant(s)					
		10/051,35	59	ASANO ET AL.				
	Office Action Summary	Examiner		Art Unit				
	·		K. Kumar	2629				
	The MAILING DATE of this communication	on appears on the	cover sheet with ti	he correspondence a	ddress			
Period fo	• •		0.51/5155 - 1/61/		>>			
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Status								
1) 又	Responsive to communication(s) filed on	o 21 Sentember 2	2006					
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,	closed in accordance with the practice under <i>Ex parte Quayle</i> , 1935 C.D. 11, 453 O.G. 213.							
	on of Claims	•	•	•				
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•	Claim(s) <u>1-7</u> is/are pending in the application. 4a) Of the above claim(s) is/are withdrawn from consideration.							
	Claim(s) is/are allowed.							
· —	☑ Claim(s) is/are allowed. ☑ Claim(s) <u>1-7</u> is/are rejected.							
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	Claim(s) are subject to restriction	and/or election re	equirement	•				
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Applicati	on Papers							
9)[] 1	The specification is objected to by the Ex	aminer.						
10) 🔲 .	The drawing(s) filed on is/are: a)[accepted or b)	objected to by t	he Examiner.				
	Applicant may not request that any objection	to the drawing(s) b	e held in abeyance.	See 37 CFR 1.85(a).				
	Replacement drawing sheet(s) including the	correction is requir	ed if the drawing(s) is	s objected to. See 37 C	FR 1.121(d).			
11) 🔲 -	The oath or declaration is objected to by	the Examiner. No	ote the attached Of	fice Action or form P	TO-152.			
Priority u	nder 35 U.S.C. § 119							
12) 🗌 /	Acknowledgment is made of a claim for fo	oreign priority un	der 35 U.S.C. § 11	9(a)-(d) or (f).				
a)[☐ All b) ☐ Some * c) ☐ None of:		•					
	1. Certified copies of the priority docu	uments have bee	n received.		•			
	2. Certified copies of the priority docu	uments have bee	n received in Appli	cation No				
	3. Copies of the certified copies of the	e priority docume	ents have been rec	eived in this Nationa	l Stage			
	application from the International E	Bureau (PCT Rul	e 17.2(a)).					
* S	ee the attached detailed Office action for	a list of the certi	fied copies not rec	eived.				
Attachment	t(s)							
_	e of References Cited (PTO-892)		4) Interview Summ	nary (PTO-413)				
2) 🔲 Notice	e of Draftsperson's Patent Drawing Review (PTO-9	948)	Paper No(s)/Ma	ail Date				
	nation Disclosure Statement(s) (PTO/SB/08) r No(s)/Mail Date		5) Notice of Inform6) Other:	nal Patent Application				
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DETAILED ACTION

The following is in response to the Amendment filed September 21, 2006. Claims 1-7 are pending and have been amended.

Claim Rejections - 35 USC § 103

- 1. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:
 - (a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negatived by the manner in which the invention was made.
- 2. Claims 1-7 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kimura et al (US 6,351,327 B1) in view of Brody (US 4,982,273).

As to independent claim 1, Kimura et al disclose an organic electroluminescent display (col. 19, line 66-col. 20, line 7, and Fig. 1, item 100) having active matrix circuitry (col. 20, lines 9-25), the light emitting display comprising; a substrate (Fig. 1, item 1); a device layer provided on the substrate (col. 20, lines 20-25), the device layer comprising a plurality of luminescent devices (Fig. 1, item 224) defining pixel units (Fig. 1, item 10) arrayed in a matrix (Fig. 1, col. 20, lines 26-40), each luminescent device having an emitting area that emits independently of the emitting areas of the other luminescent devices (col. 1, lines 39-44, where in Fig. 1, item 224 shows the individual luminescent device per pixel independent from other pixel emitting areas); a circuitry layer provided between the substrate (Fig. 1) and the device layer, the circuitry layer comprising pixel circuits for driving the respective luminescent devices (col. 1, lines 24-58), the pixel circuits defining the pixel units (Fig. 1, item 10, col. 20, lines 26-40); Kimura et al do not disclose contacts, electrically connecting each of the luminescent devices with a corresponding

pixel circuit, wherein the contacts are not provided under the emitting area of the luminescent devices. Brody discloses contacts (Figs 4b and 4c, item 27, col. 7, line 54-col. 8, lines 18), wherein the contacts are not provided under the emitting area of the luminescent devices, as in Fig. 4b, the contacts (27) are shown to be at the edges of the emitting areas. It would have been obvious to one of ordinary skill in the art to include the contacts of Brody into Kimura et al as the contacts for the row or column of the display as disclosed by Brody in col. 2, lines 65-col. 3, lines 20 improve image quality.

As to independent claim 6, limitations of claim 1, and further comprising, Kimura et al disclose an organic layer including a luminescent layer and lying between the upper electrode and the lower electrode (col. 20, lines 26-40). Kimura et al do not disclose wherein each lower electrode has a contact electrically connecting the corresponding luminescent device with the corresponding pixel circuit, and wherein the upper electrode is not provided over the contact. Brody discloses wherein each lower electrode has a contact electrically connecting the corresponding luminescent device with the corresponding pixel circuit (Figs 4b and 4c, item 27, col. 7, line 54-col. 8, lines 18), and wherein the upper electrode is not provided over the contact, as in Fig. 4b, the contacts (27) are shown to be at the edges of the emitting areas. It would have been obvious to one of ordinary skill in the art to include the contacts of Brody into Kimura et al as the contacts for the row or column of the display as disclosed by Brody in col. 2, lines 65-col. 3, lines 20 improve image quality.

As to dependent claim 2, limitations of claim 1, and further comprising, Brody discloses a flat screen color display comprising an active matrix and wherein the contacts are arrayed in a

single dimension for each row or column in the matrix in Figs. 4a and 7 and in col. 7, line 54-col. 8, lines 18.

As to dependent claim 3, limitations of claim 2, and further comprising, Brody discloses a flat screen color display comprising an active matrix and wherein the contacts for the pixel units belonging to two adjacent rows or columns in the matrix are arrayed in a single dimension between the two adjacent rows or columns in Figs. 4a and 7 and in col. 7, line 54-col. 8, lines 18.

As to dependent claim 4, limitations of claim 1, and further comprising, Kimura et al disclose wherein the luminescent devices are organic electroluminescent devices (col. 20, lines 29-30), each comprising a first electrode, a second electrode and an organic layer including an luminescent layer and lying between the first electrode and the second electrode (col. 20, lines 41-62).

As to dependent claims 5 and 7, limitations of claims 1 and 6, and further comprising, Kimura et al disclose wherein the pixel circuits (Fig. 1, item 10) each comprise a thin film transistor (Fig. 1, item 223, col. 20, lines 26-40).

Response to Arguments

3. Applicant's arguments filed September 21, 2006 have been fully considered but they are not persuasive.

Applicant argues where the prior art of Kimura in view of Brody fail to teach an OLED display having contacts that electrically connect luminescent devices with a corresponding pixel circuit, wherein the contacts are not provided under the emitting area of the luminescent devices. Examiner, respectfully, disagrees. Brody teaches contacts (Figs 4b and 4c, item 27, col. 7, line 54-col. 8, lines 18), wherein the contacts are not provided under the emitting area of the

luminescent devices, as in Fig. 4b, the contacts (27) are shown to be at the edges of the emitting areas. It would have been obvious to one of ordinary skill in the art to include the contacts of Brody into Kimura et al as the contacts for the row or column of the display as disclosed by Brody in col. 2, lines 65-col. 3, lines 20 improve image quality. With respect to applicant's arguments on where Brody fails to relate to an OLED display, examiner, respectfully, disagrees. Brody teaches where the contacts are also used with electroluminescent displays in col. 2, lines 53-62, therefore, the switching circuits shown by one of the embodiments of Brody would correlate to the luminescent devices. Therefore, the rejection is maintained and made FINAL.

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Conclusion

4. **THIS ACTION IS MADE FINAL.** Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

A shortened statutory period for reply to this final action is set to expire THREE MONTHS from the mailing date of this action. In the event a first reply is filed within TWO MONTHS of the mailing date of this final action and the advisory action is not mailed until after the end of the THREE-MONTH shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than SIX MONTHS from the mailing date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Srilakshmi K. Kumar whose telephone number is 571 272 7769.

The examiner can normally be reached on 9:00 am to 5:30 pm.

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If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Sumati Lefkowitz can be reached on 571 272 3638. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Srilakshmi K. Kumar Examiner Art Unit 2629

SKK December 8, 2006

SUMATI LEFKOWITZ
SUPERVISORY PATENT EXAMINER